

Figure 1b

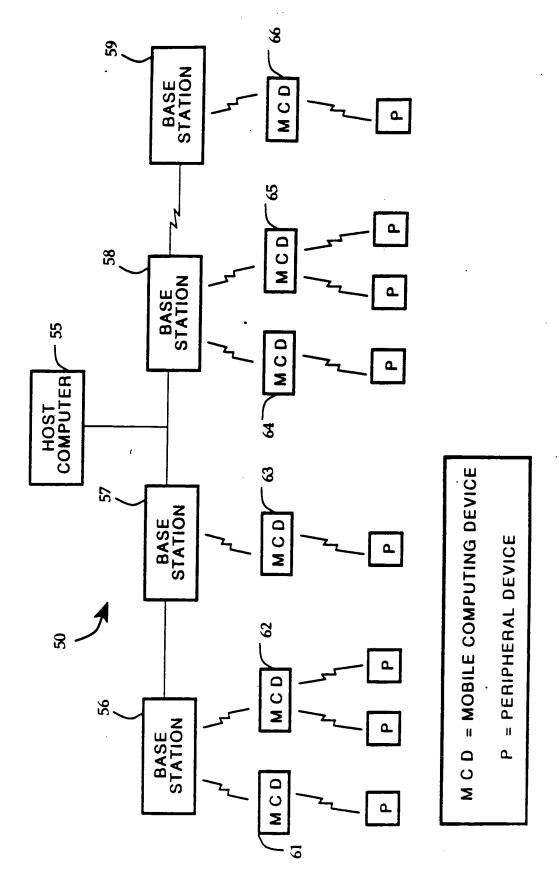
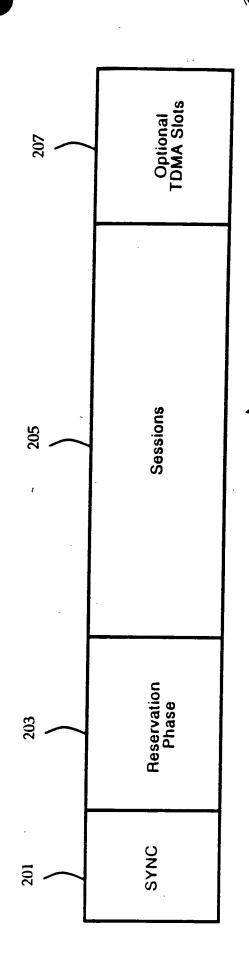


Figure 1c



200

Figure 2

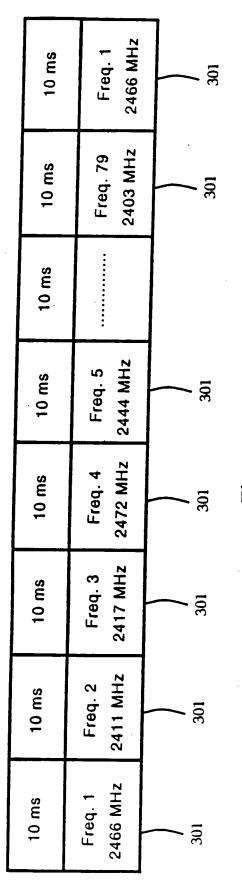


Figure 3a

i		<del></del>		7	
	20 ms	Freq. 1	2466 MHz		303
	20 ms	Freq. 79	2403 MHz		303
	20 ms				
	20 ms	Freq. 5	2444 MHz		303
	20 ms	Freq. 4	2472 MHz	<u> </u>	303
	20 ms	Freq. 3	2417 MH2	<u> </u>	303
	20 ms	Freq. 2	2411 MHZ	<u> </u>	303
	20 ms	Freq. 1	71.1101 004.7	<u> </u>	30.3

Figure 3b

7	2 ns
Freq 5: 2444 MHz	AI. 2 20 ms
Freq 5:	AI. 1 20 ms
417 MHz	AI. 2 20 ms
Freq 3: 2417 MHz	Al. 1 20 ms
2: 2411 MHz	AI. 2 20 ms
Freq 2: 24	Al. 1 20 ms
466 MHz	AI. 2 20 ms
Freq 1: 2466 MHz	Al. 1 20 ms

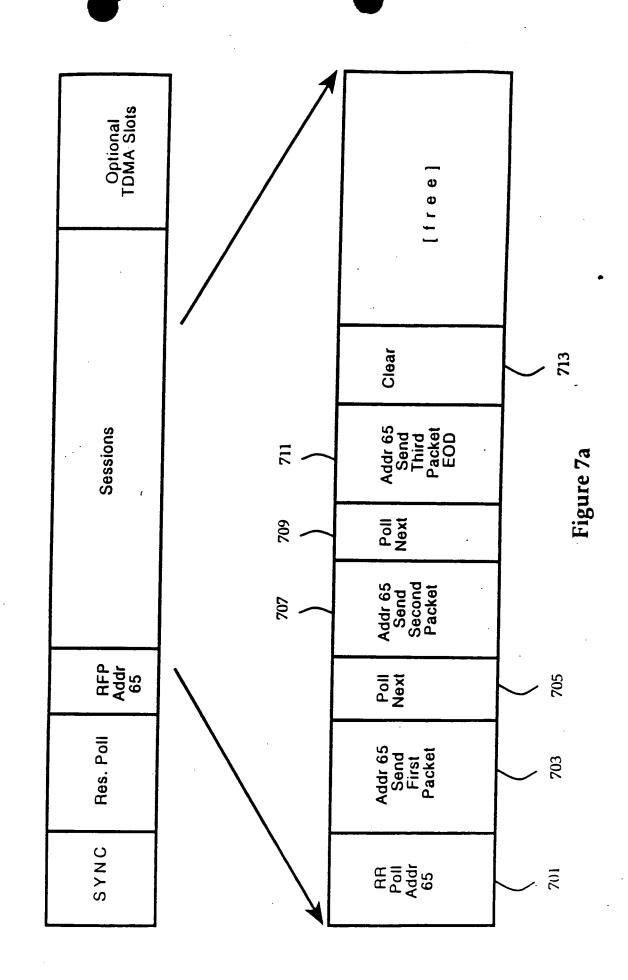
Figure 4a

Al. 3 Al. 4 Al. 1 Al. 2 Al. 3 20 ms 20 ms 20 ms 20 ms	240	rreq 1: 2466 MHz	,		Freq 2:	Freq 2: 2411 MHz	
	AI. 2 20 ms		AI. 4 . 0 ms	Al. 1 20 ms	AI. 2 20 ms	AI. 3	AI. 4

Figure 4b

Figure 5b

	Optional TDMA Slots		Optional TDMA Slots	
	Sessions	•	Sessions	
001	Slot n	Figure 6a 5	Slot	, Figure 6b
		Fig 605 (	RFP Addr 111 RFP Addr 02	) 607 Fig
601	Slot 2		Slot 2	
109	Slot 1	603	RFP Addr 19	
	Res. Poll		Res. Poll	
	SYNC		S × N C	



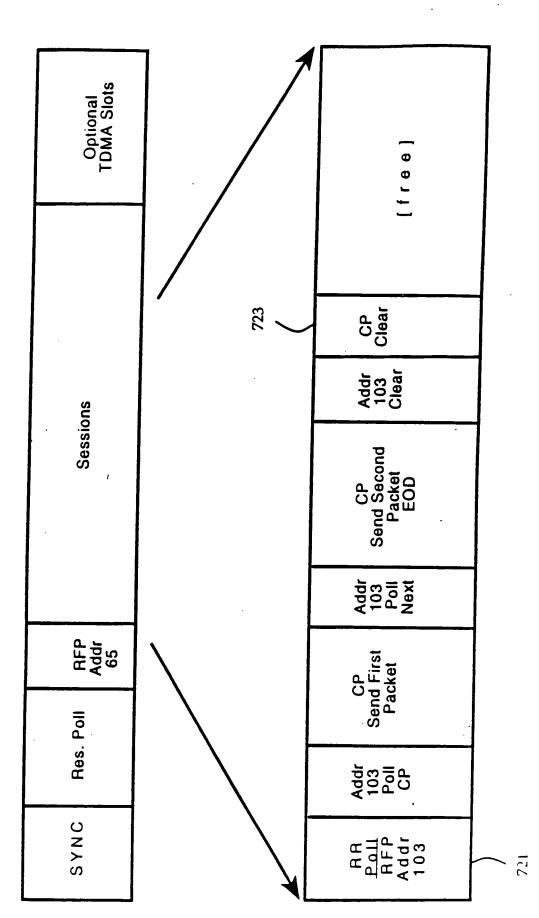
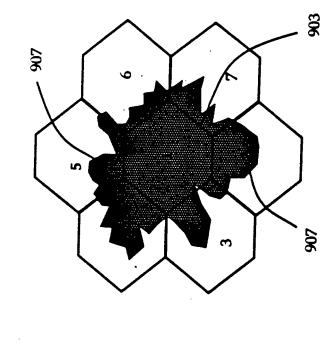


Figure 7b

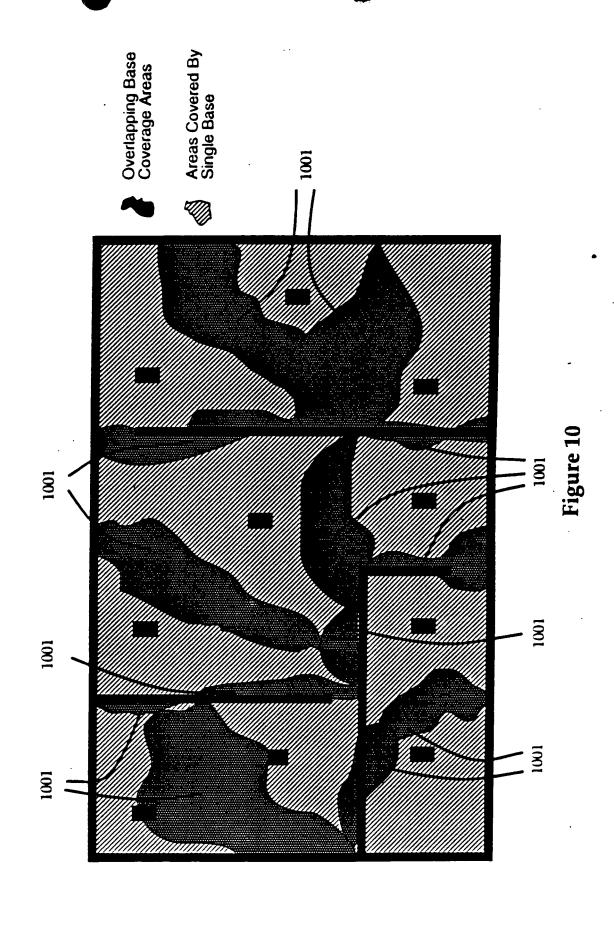
100

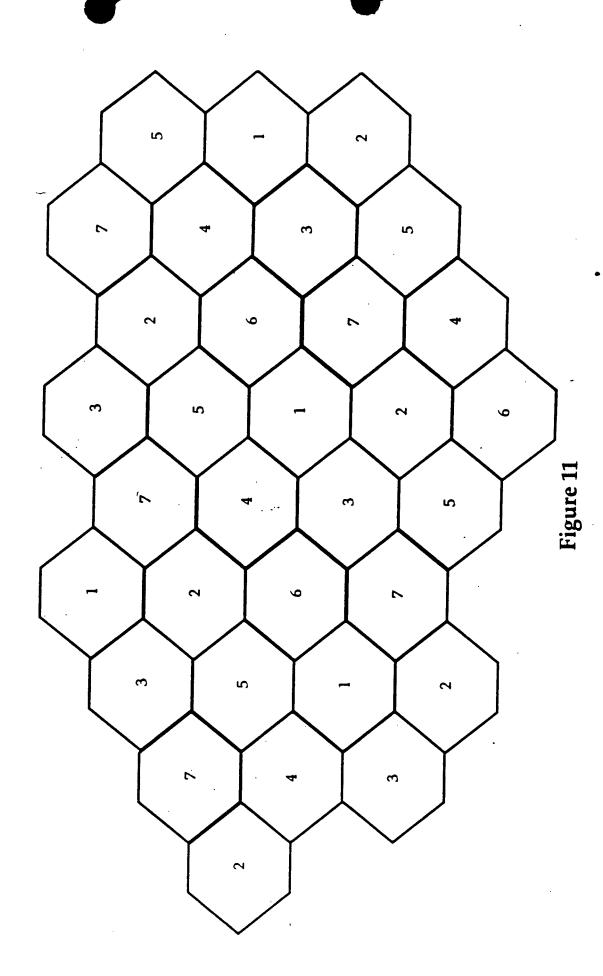
Figure 8

Figure 9b



905
905
906
907
908
908
908
908
909
908
908





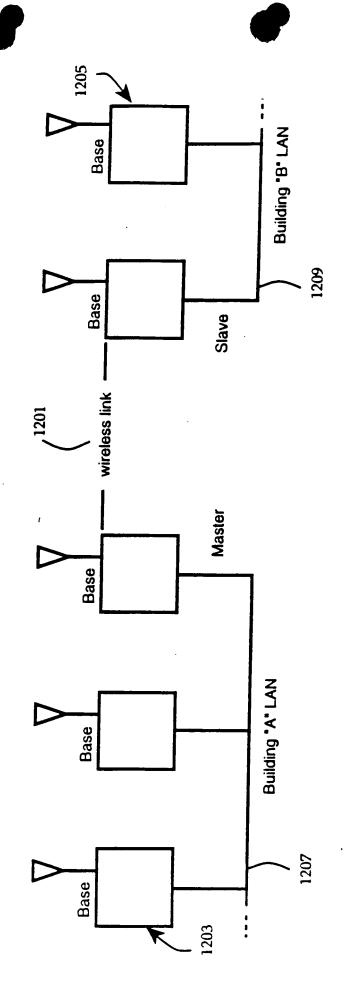


Figure 12

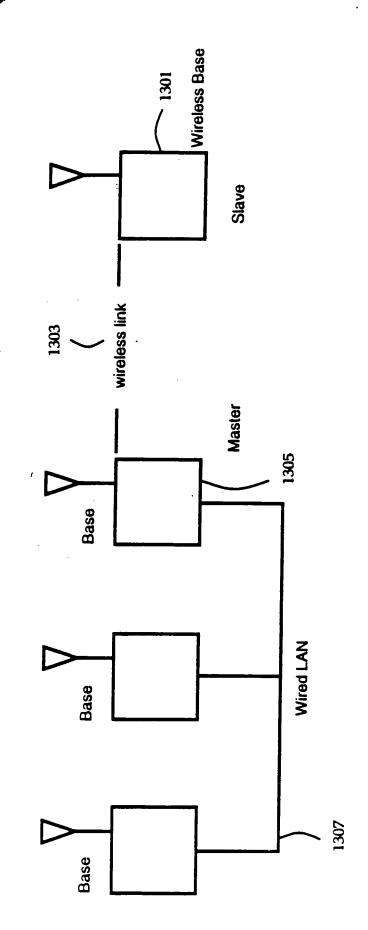
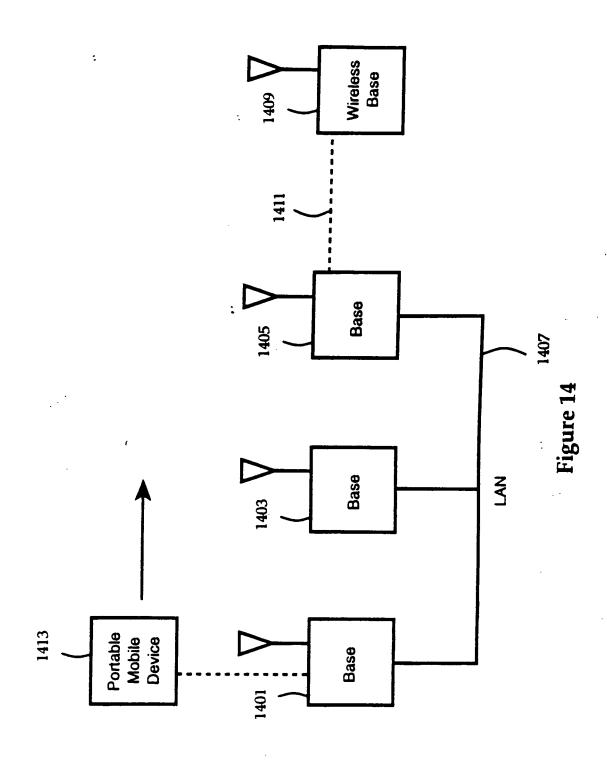


Figure 13



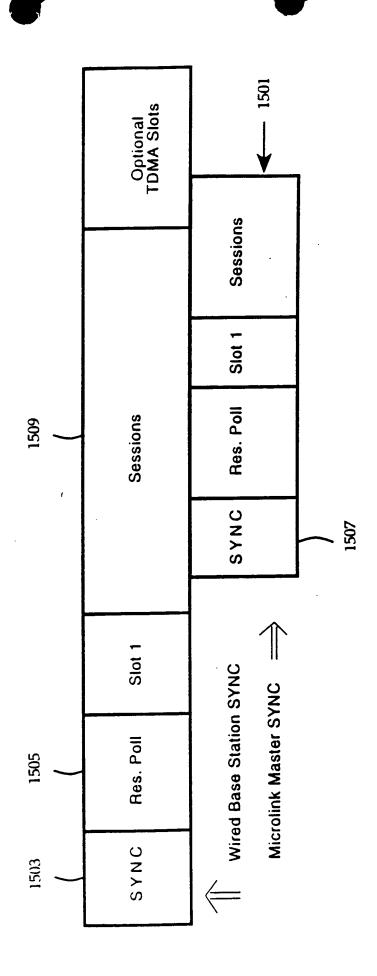


Figure 15

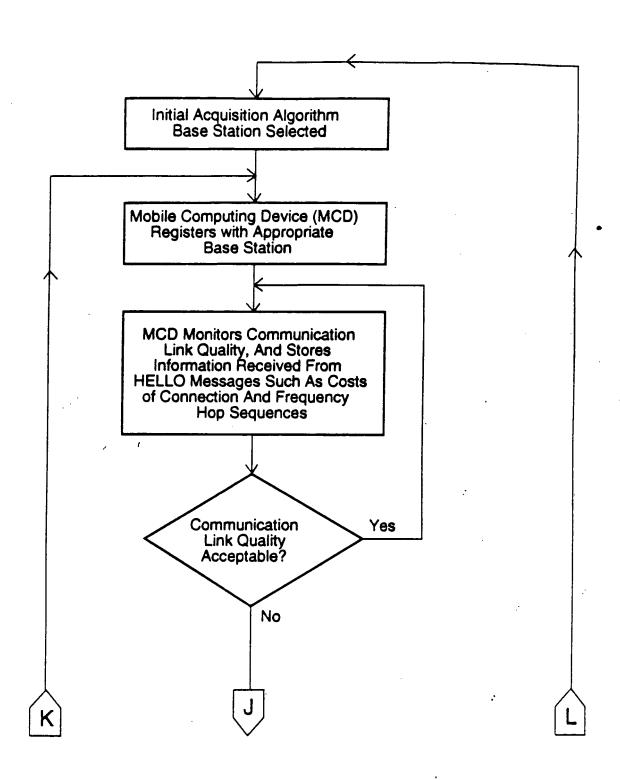
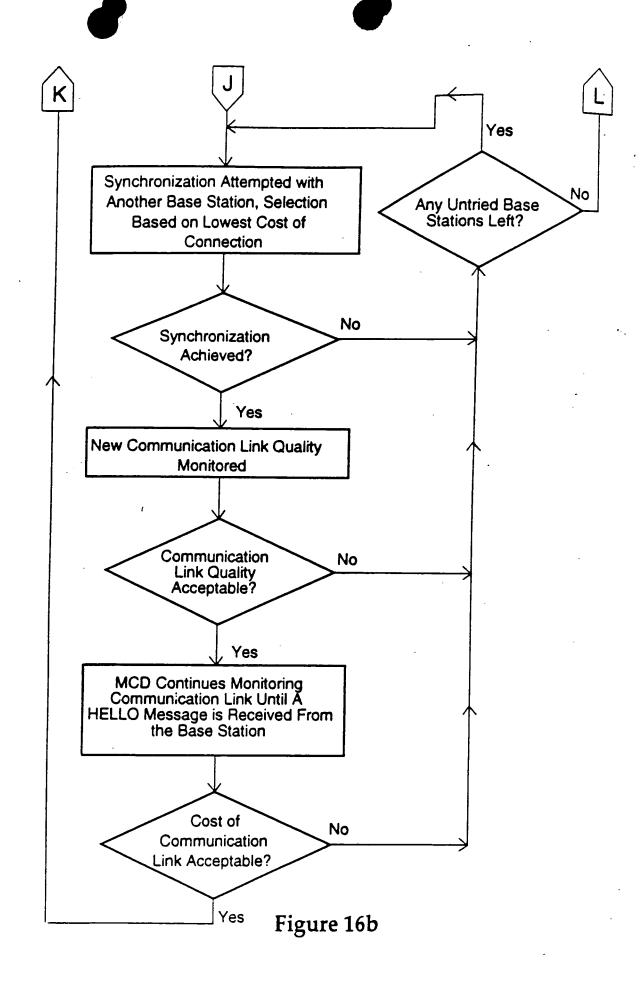


Figure 16a



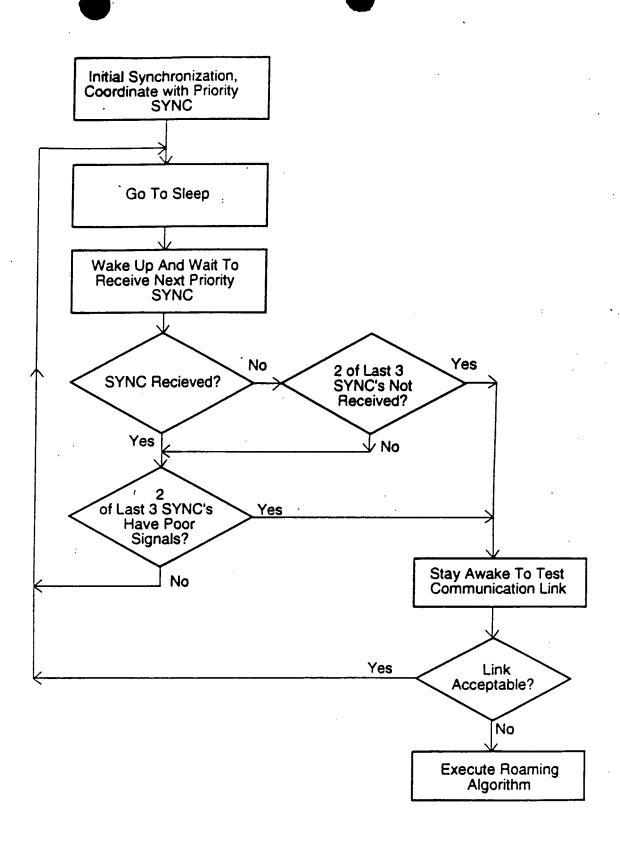
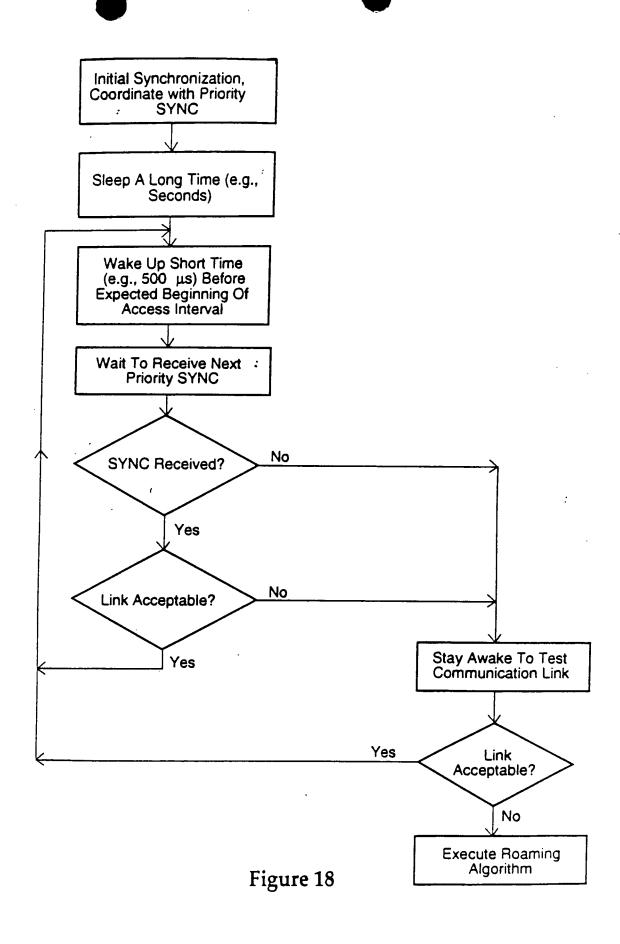
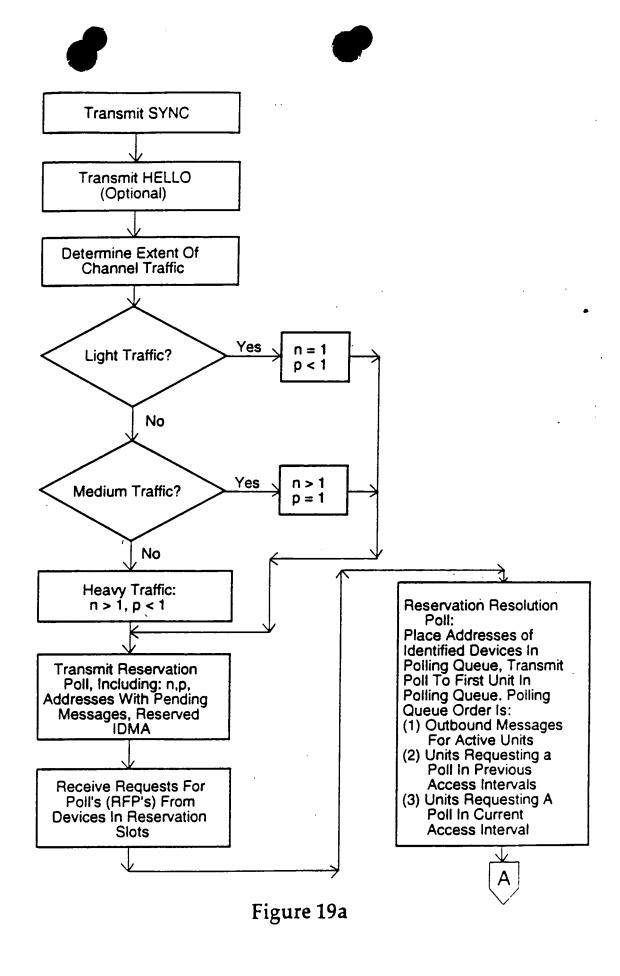


Figure 17





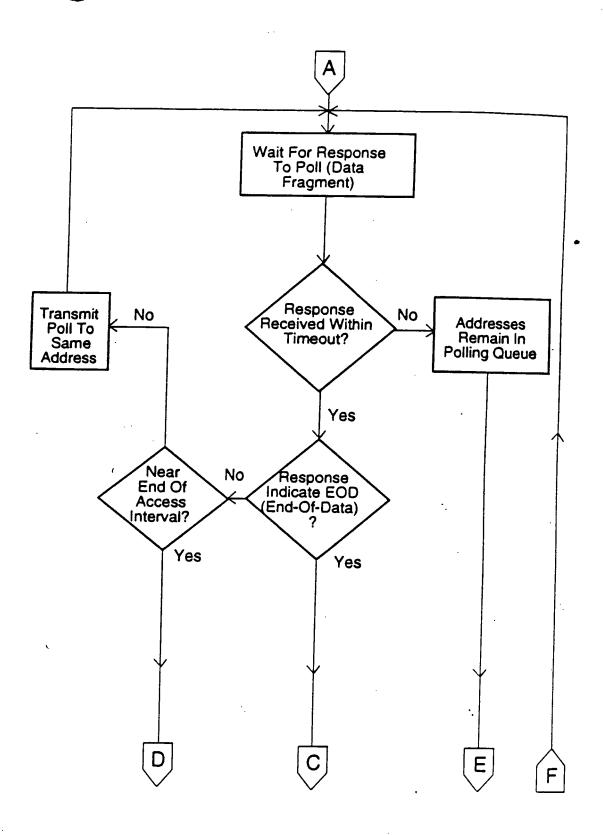


Figure 19b-1

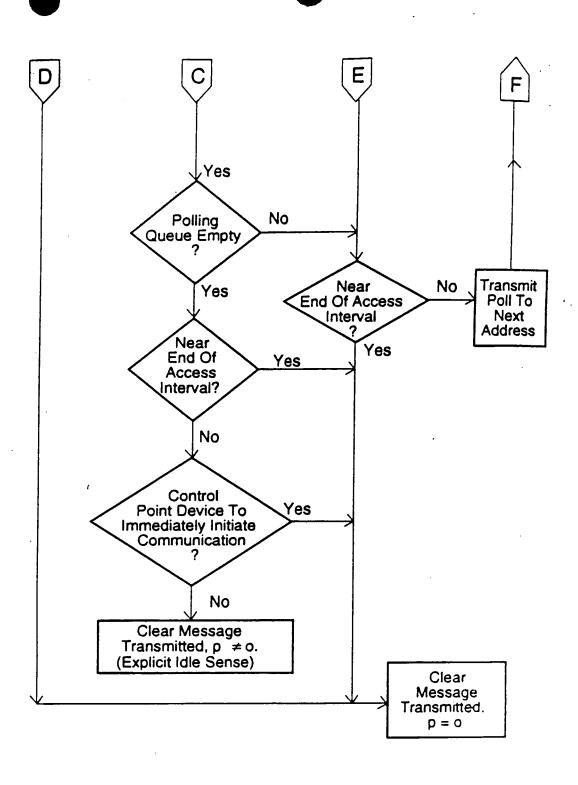


Figure 19b-2

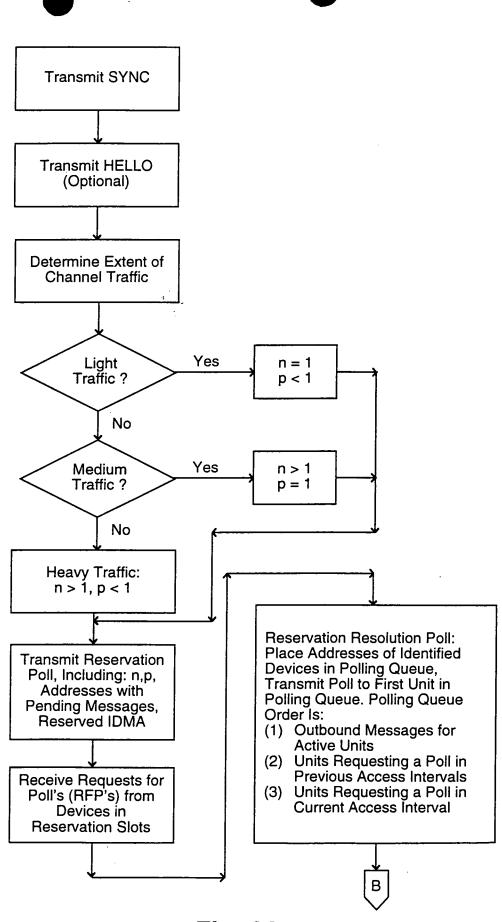


Fig. 20a

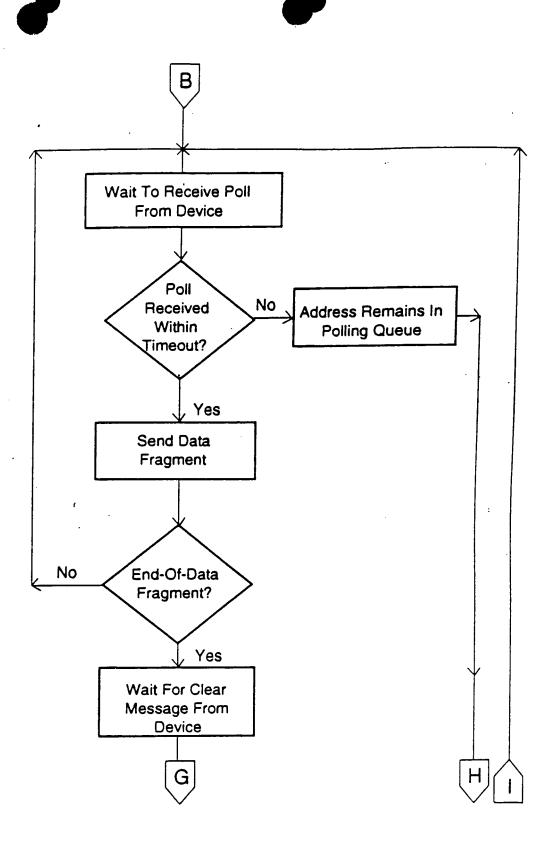


Figure 20b-1

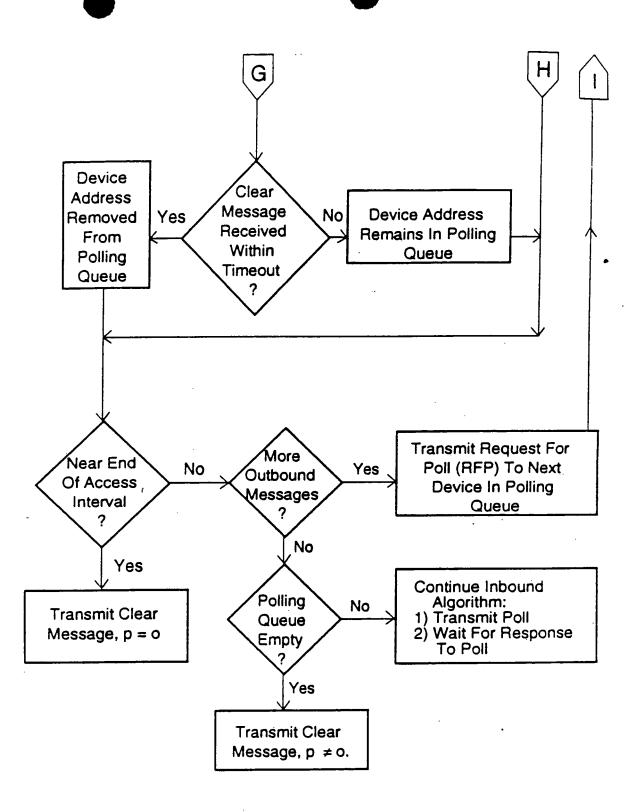
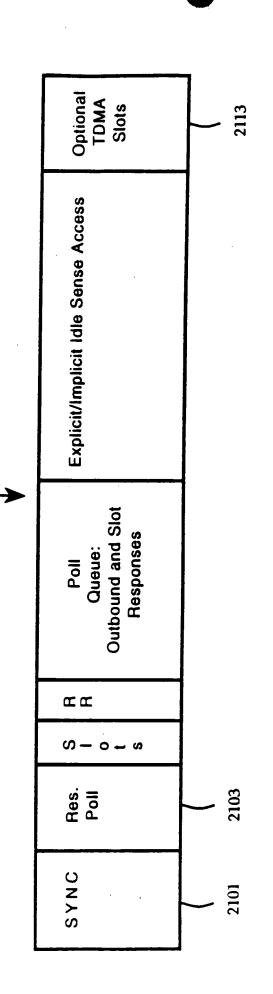


Figure 20b-2



2100

Figure 21

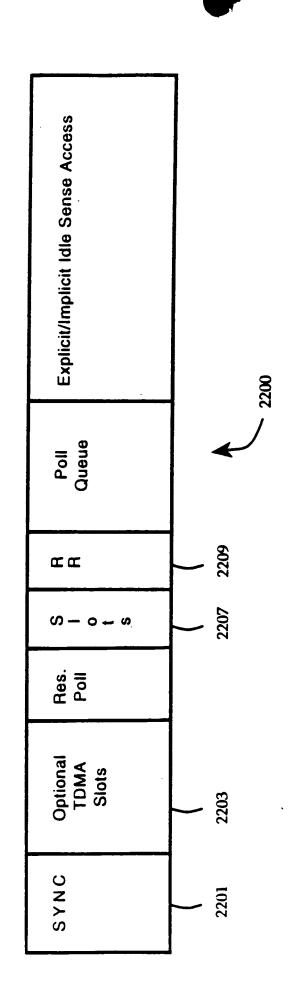


Figure 22

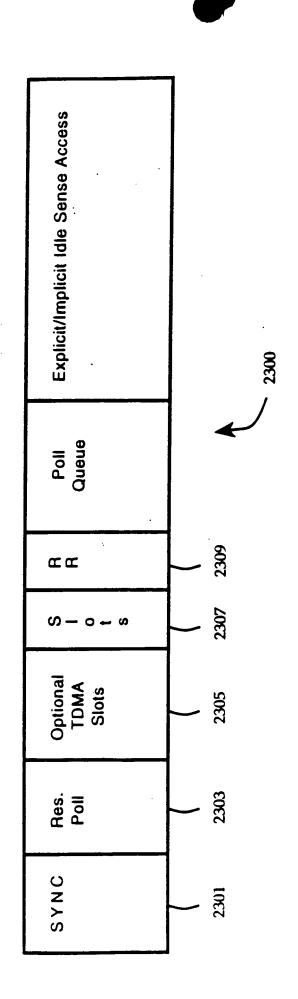


Figure 23

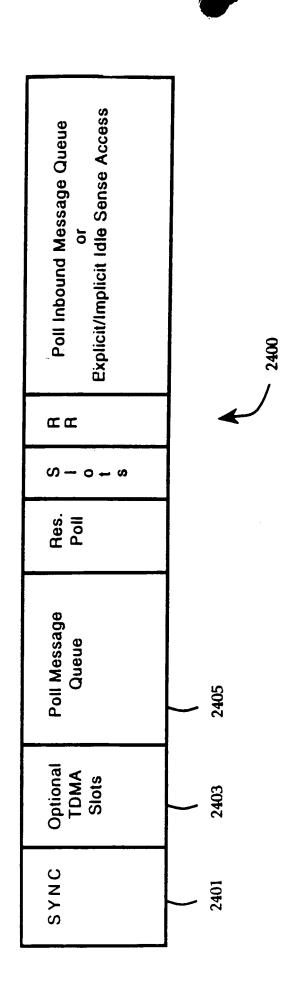
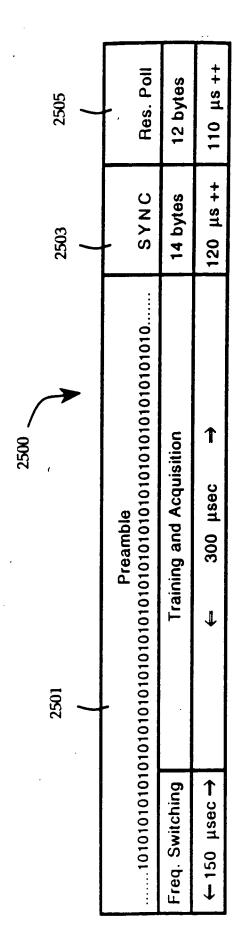


Figure 24



++ allows additional bit times for HDLC zero bit insertion

Figure 25

SYNC

J 2600

Byte	Content	Definition
1	FLAG	Frame Delimiter
2	Dest. Address 1	Destination Address (SYNC = Broadcast = FFFF <sub>H</sub> )
3	Dest. Address 2	Destination Address, 2nd Byte
4	TYPE	Message Type=SYNC, Priority Sync, or Priority SYNC with
5	Source Address 1	Control Point Address
9	Source Address 2	Control Point Address, 2nd Byte
7*	SYSTEM ID	System Identifier (LAN ID)
8*	NET ID	Ol 19N
<b>.</b> 6	Command/Status 1	Control Point Status
		2 Bits Control Point Type
		2 Bits Loading Factor
		1 Bit Scheduled Service Availability
		3 Bits Reserved
10*	Command/Status 2	Scheduled Services
		5 Bits Number of Allocated TDMA Time Slots
		3 Bits Number of Available Time Slots
11*	Random	Randomly Selected Character for Improved Encryption
12	CRC 1	Error Check
13	CRC 2	Error Check
14	FLAG	Frame Delimiter

Figure 26

\*Optionally Encrypted Bytes

## Reservation Poll

- 2700

Definition	Frame Delimiter	Destination Address (RES = Broadcast = FFFF <sub>H</sub> )	Destination Address, 2nd Byte	Message Type = RES POLL	3 Bits Number of Slots	5 Bits Probability Factor	Hopping Sequence ID	Hopping Sequence Index		3 Bits Hop Rate	2 Bits Al's per Hop	3 Bits Reserved	Error Chack	Frror Chack	Frame Delimiter
Byte Content	FLAG	Dest. Address 1	Dest. Address 2	TYPE	SLOT PARMS		Sequence	yepul	NET PARMS				CRC 1	CRC 2	FLAG
Вуте	1	2	3	4	5	9	7*	8•	<b>.</b> 6				10	11	12

## Figure 27

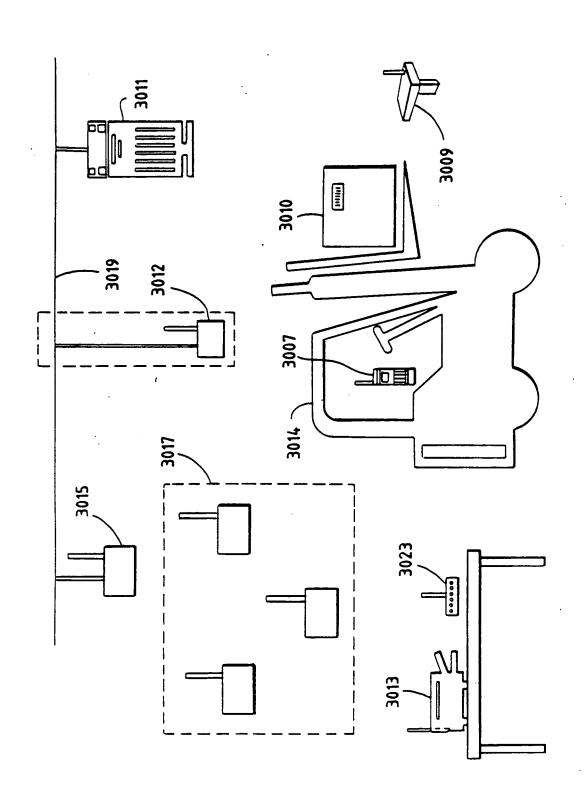
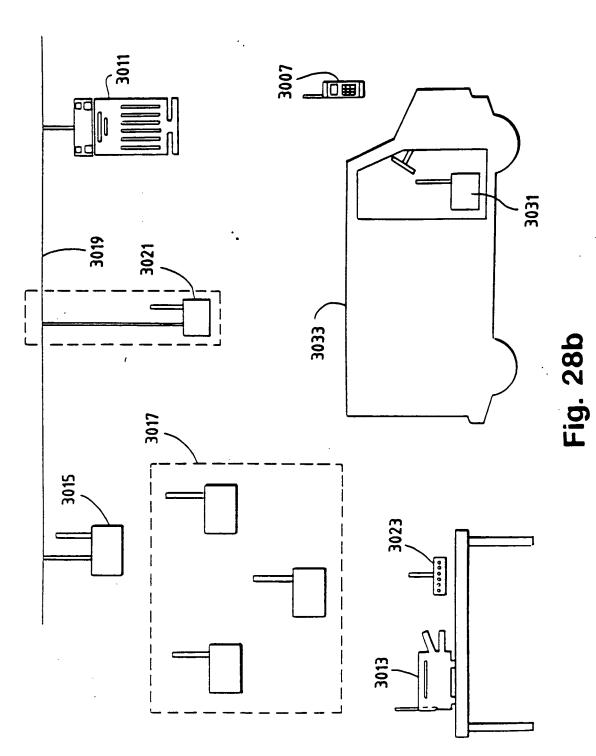
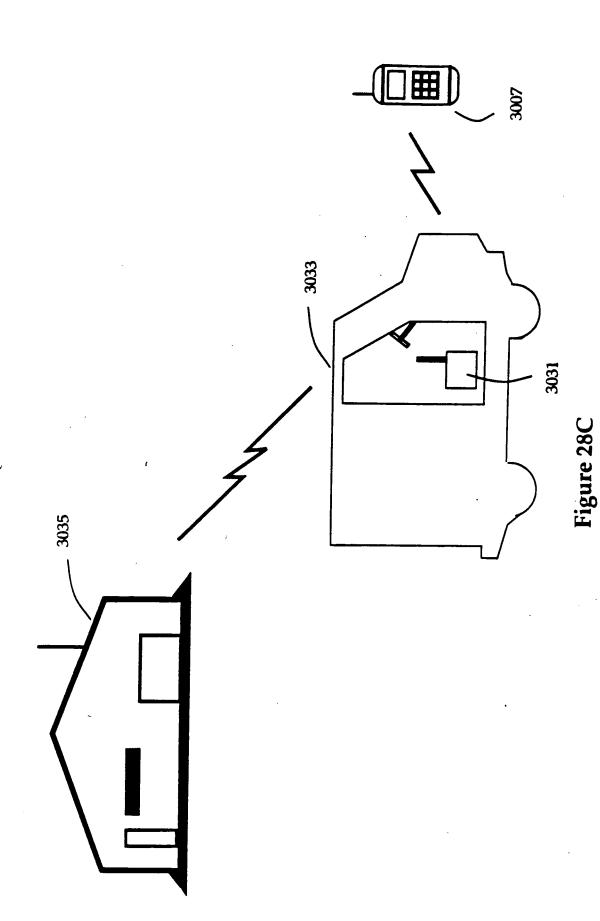


Fig. 28A





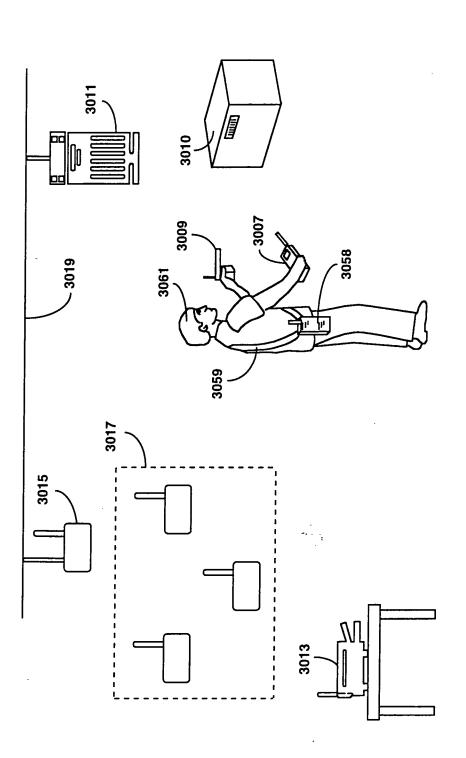


Fig. 29A

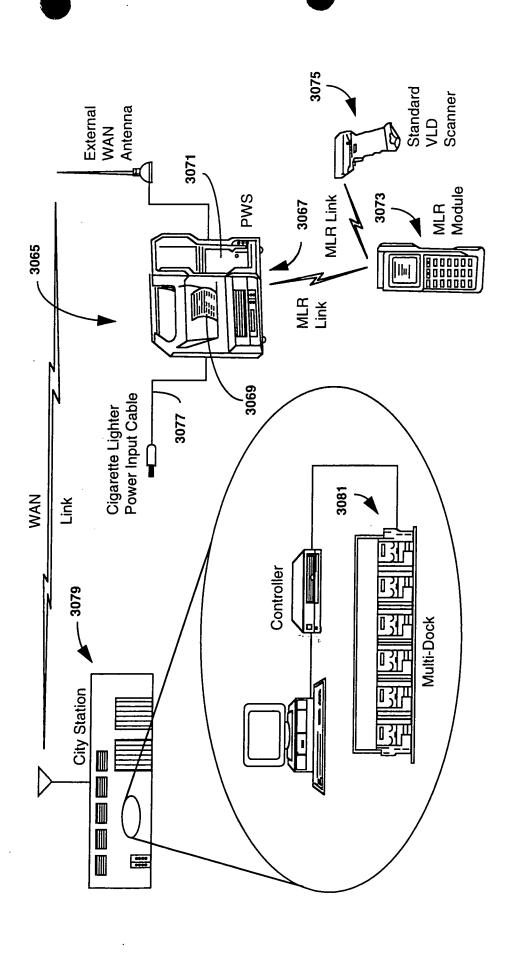


Fig. 29B

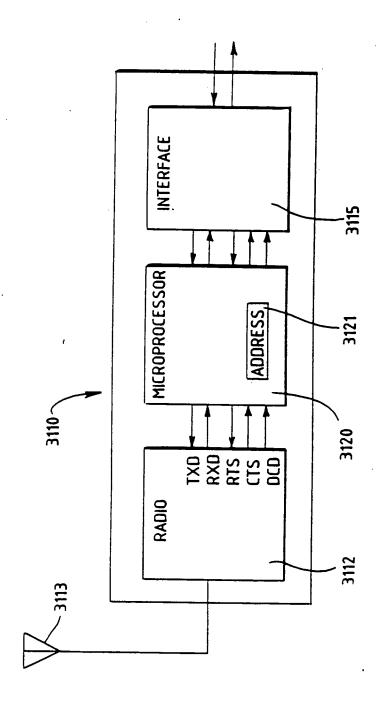


Fig. 30

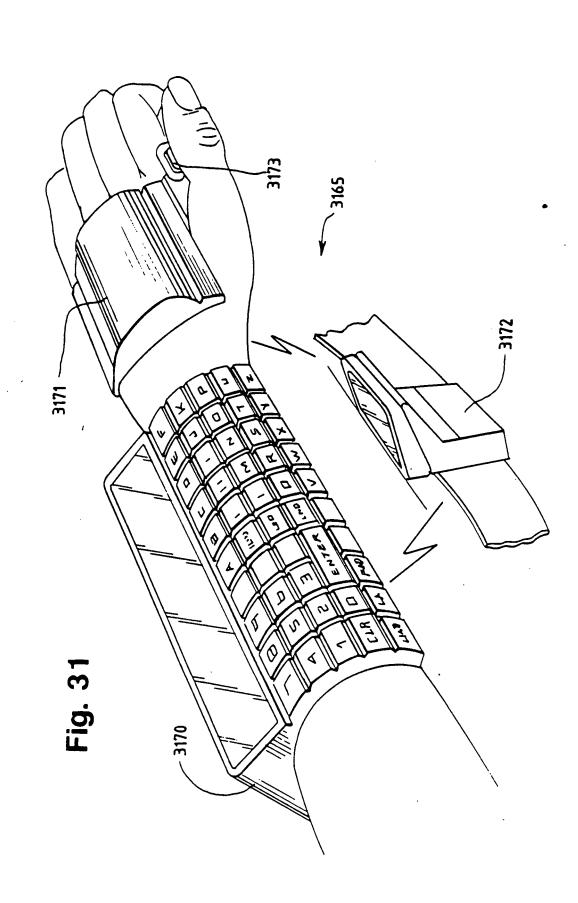
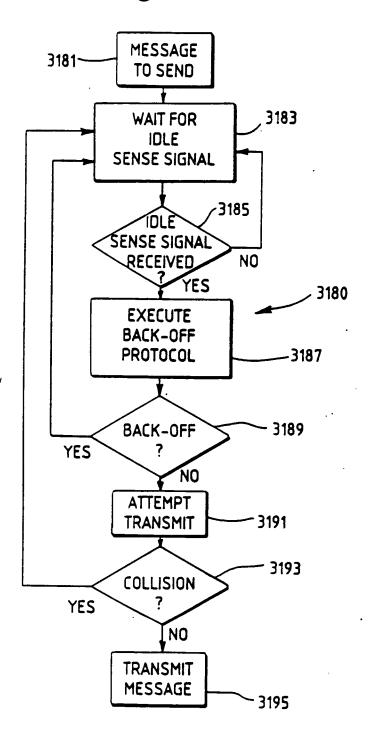


Fig. 32



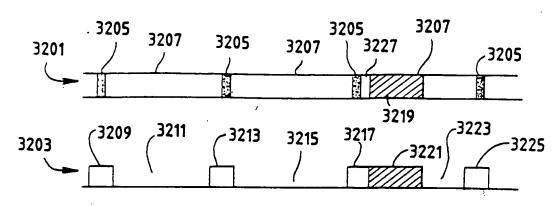
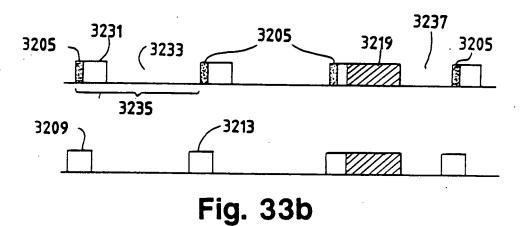
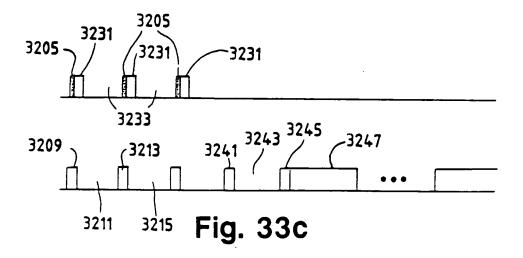


Fig. 33a





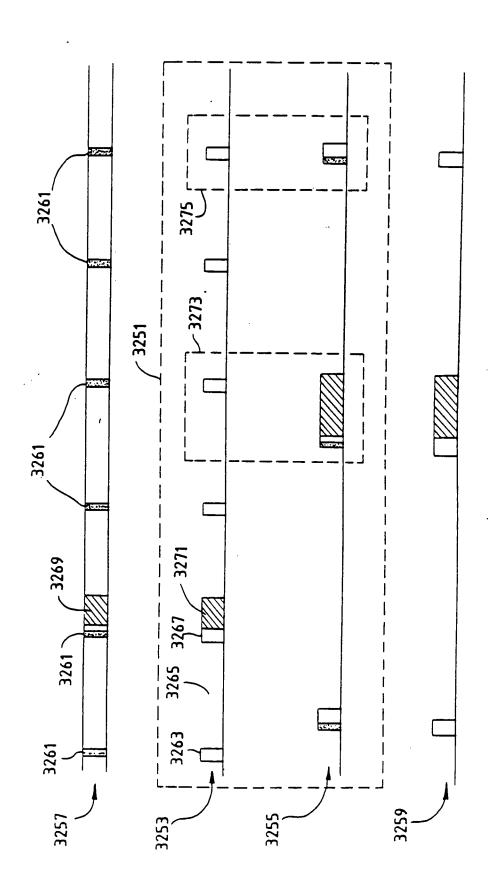


Fig. 34

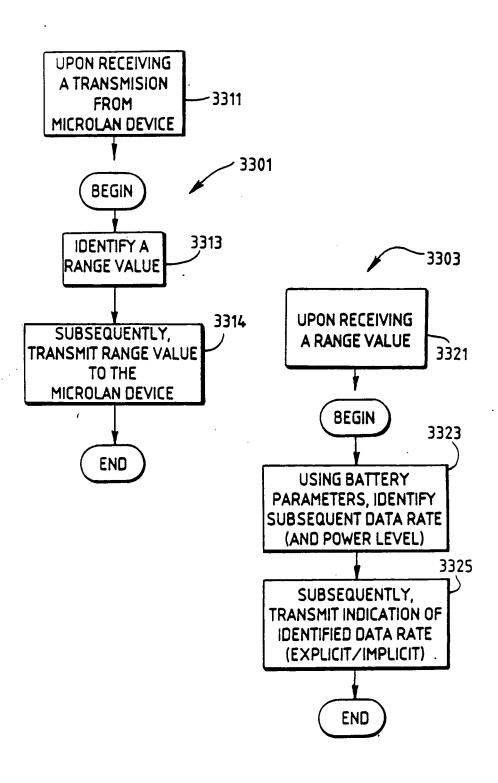
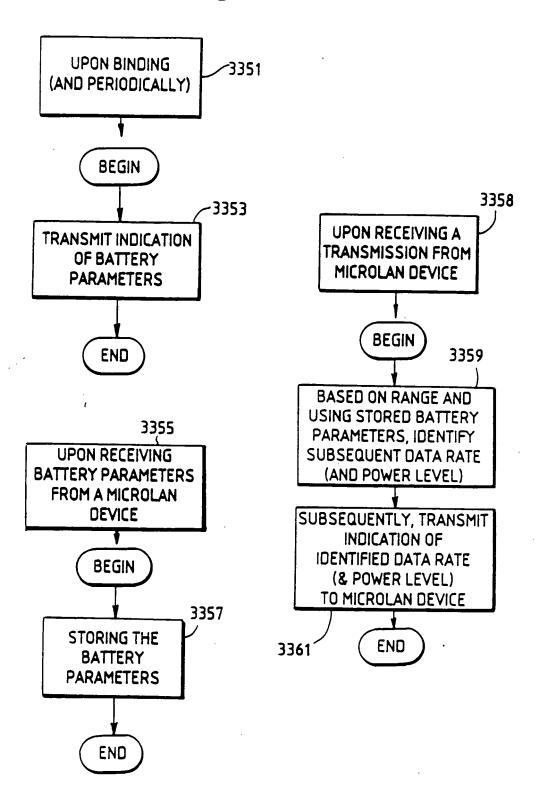


Fig. 35

Fig. 36



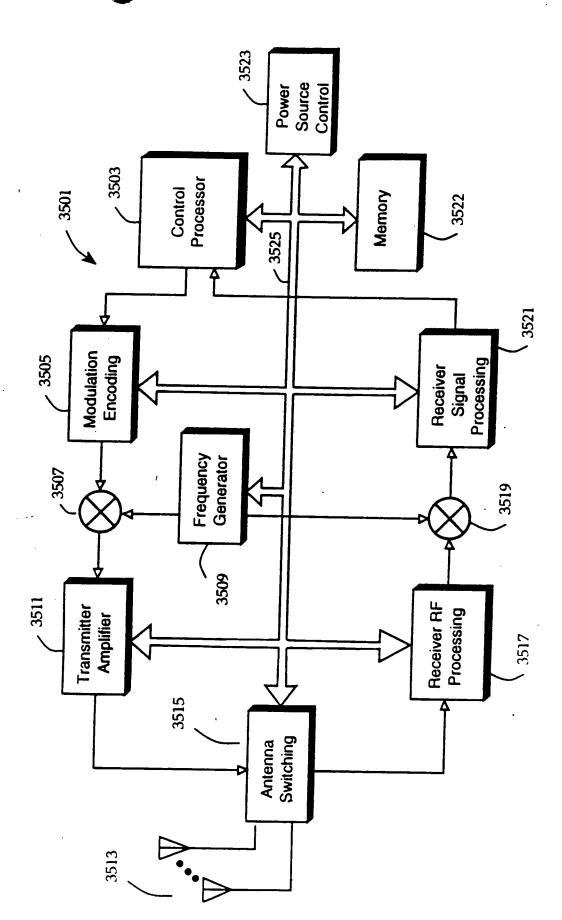


Figure 37

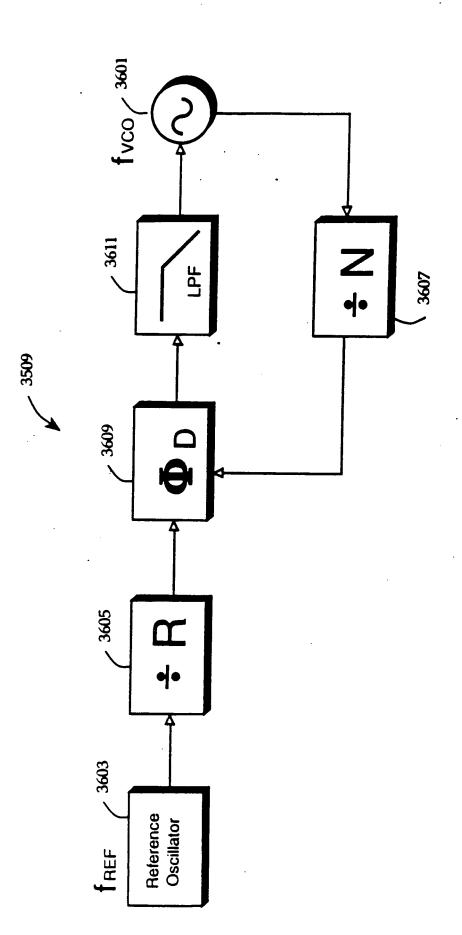


Figure 38

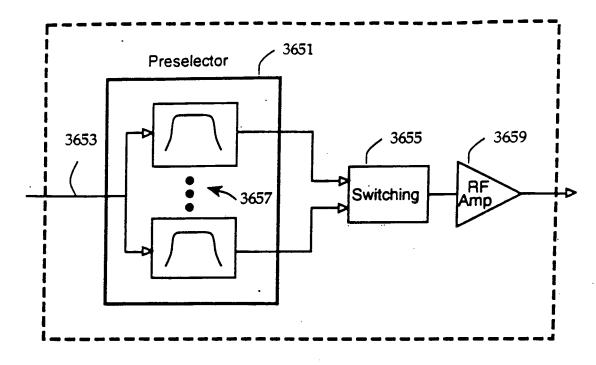


Figure 39

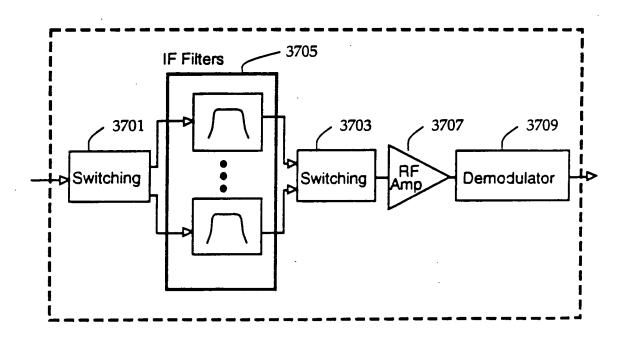


Figure 40

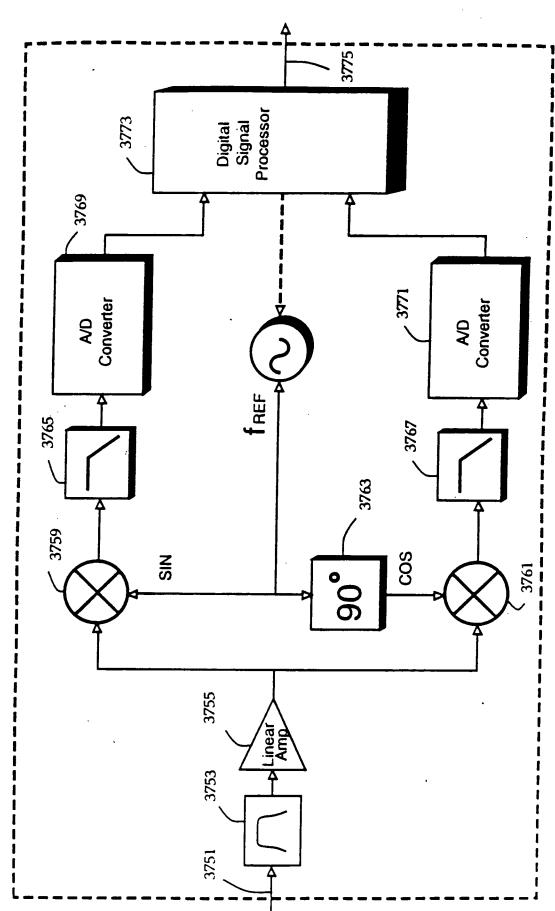


Figure 41

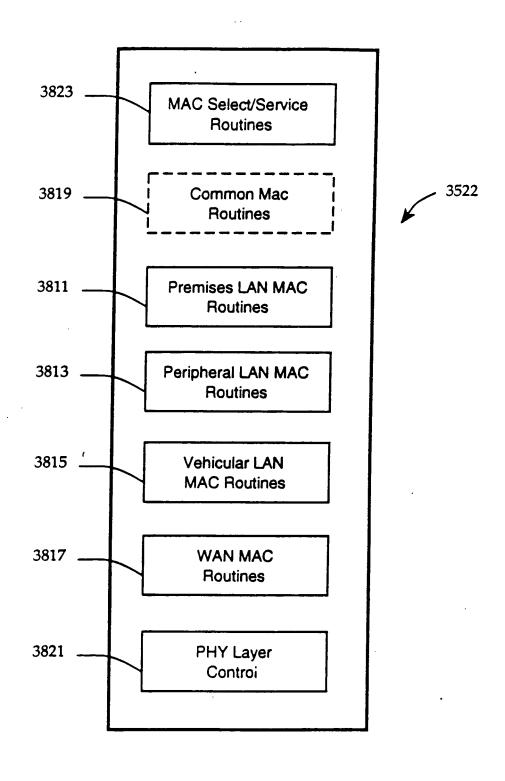


Figure 42

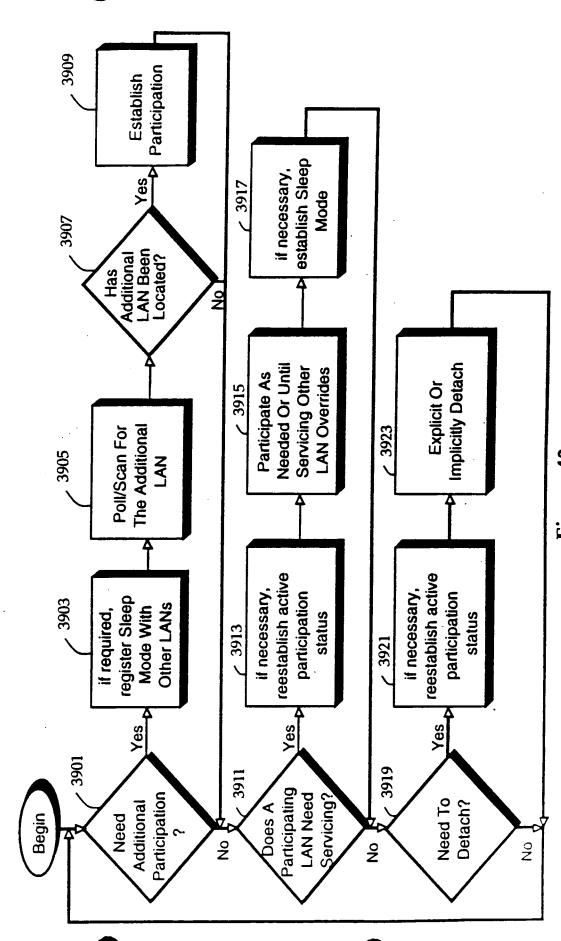


Figure 43

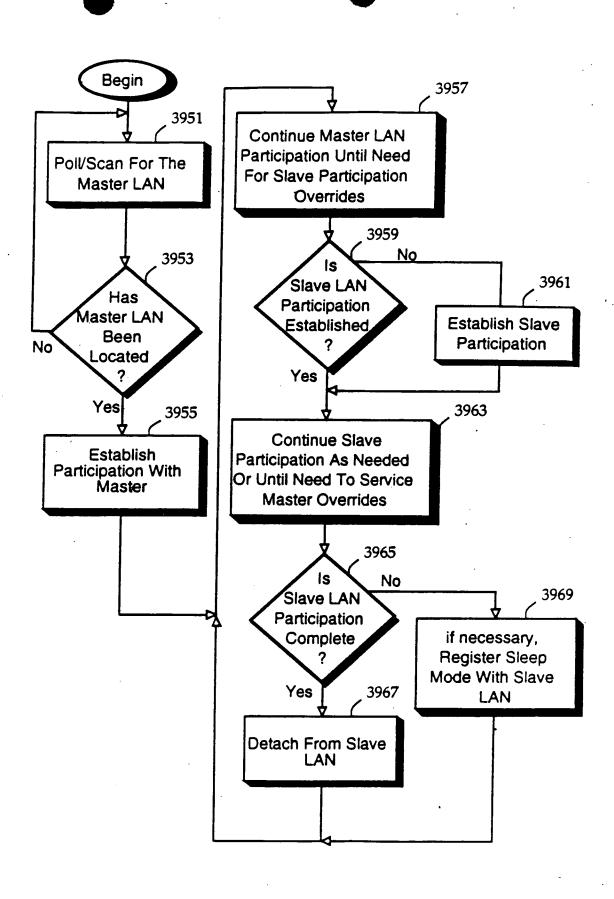


Figure 44

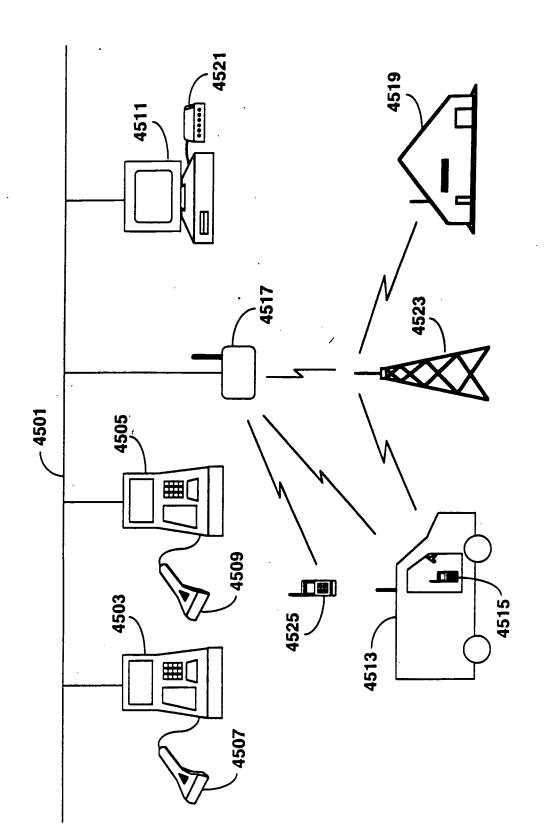


Fig. 45

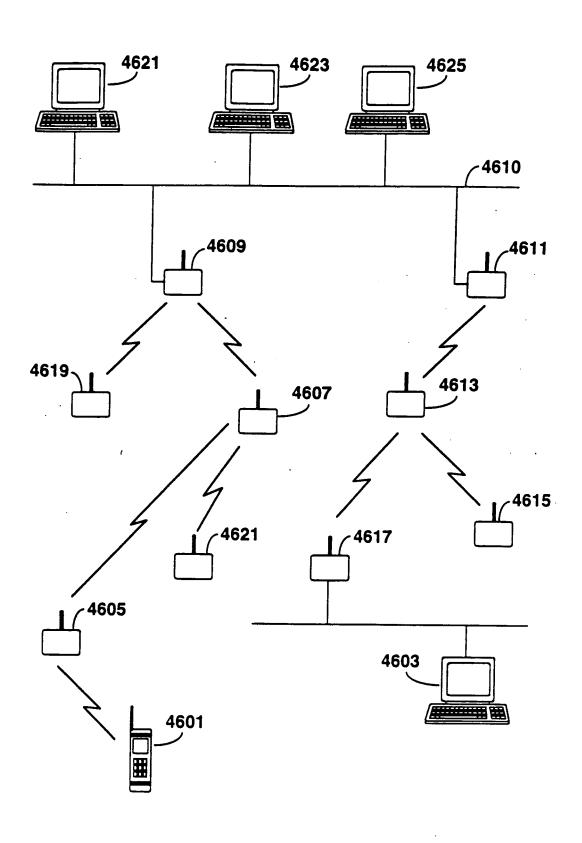


Fig. 46a

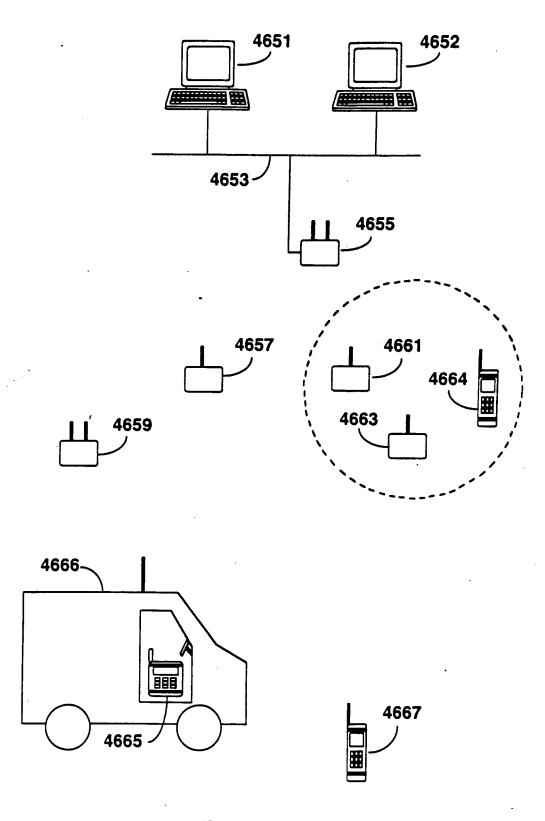


Fig. 46b

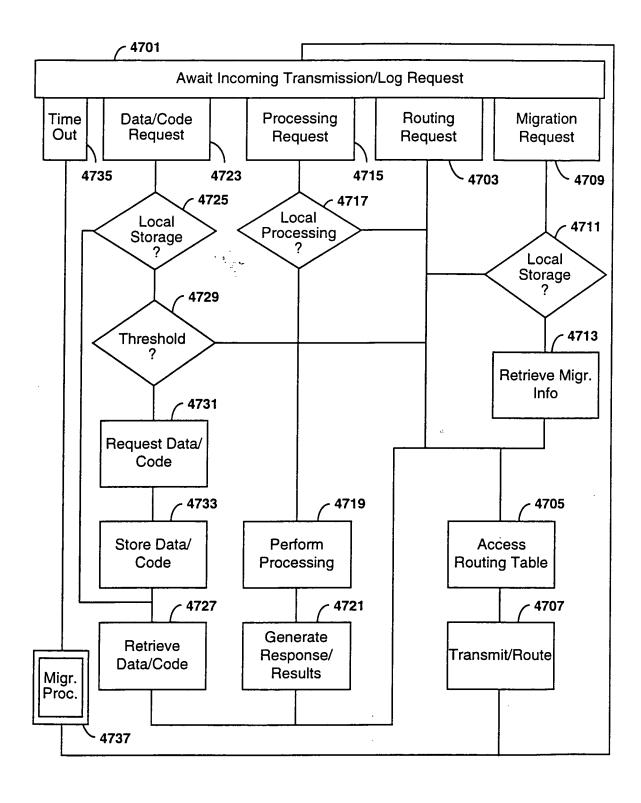


Fig. 47a

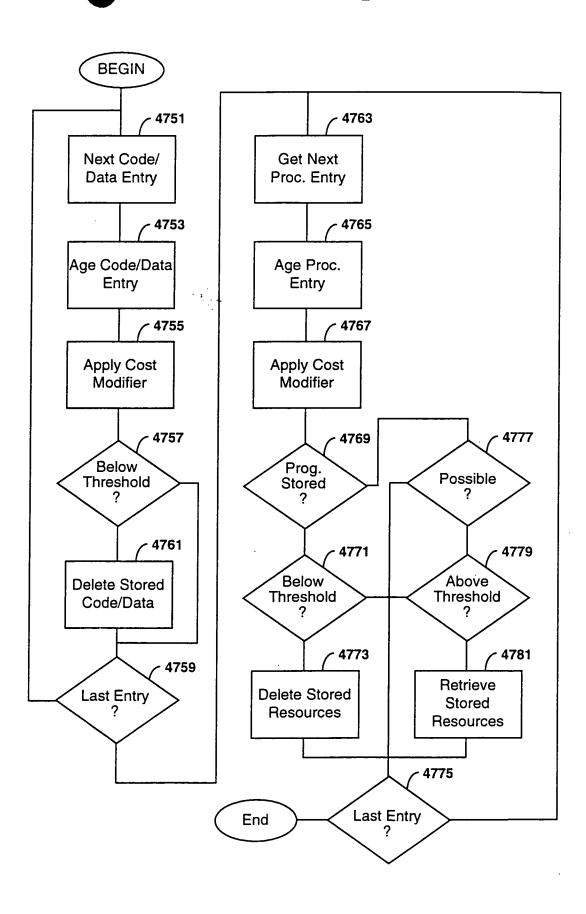
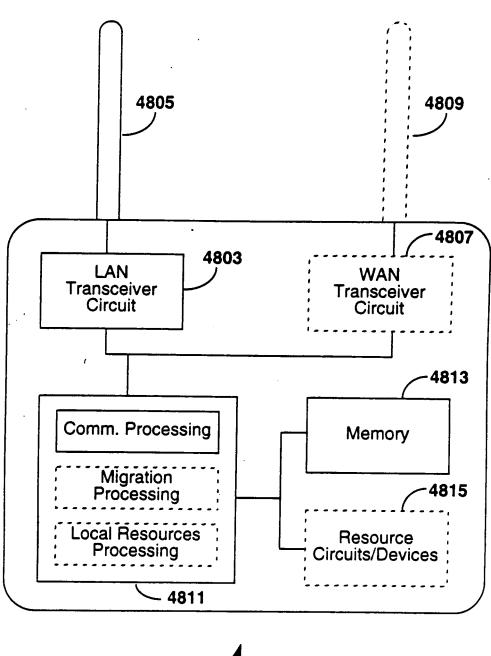


Fig. 47b



4801

Fig. 48

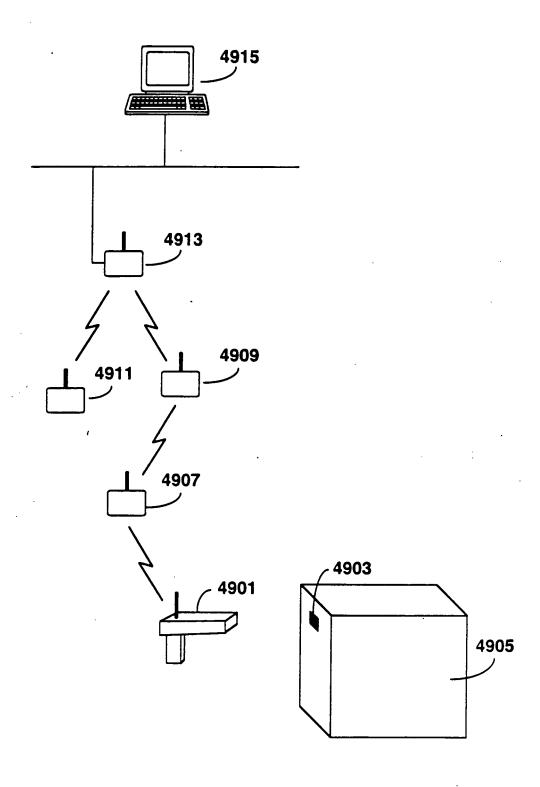


Fig. 49a

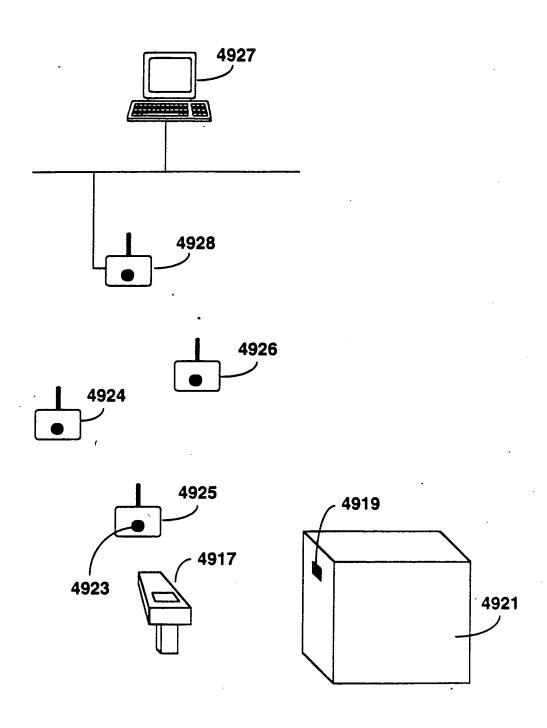


Fig. 49b

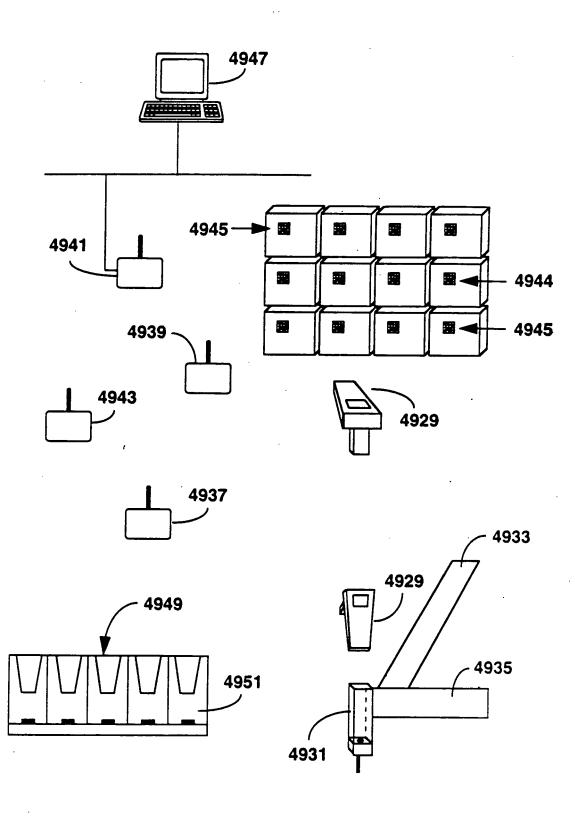


Fig. 49c

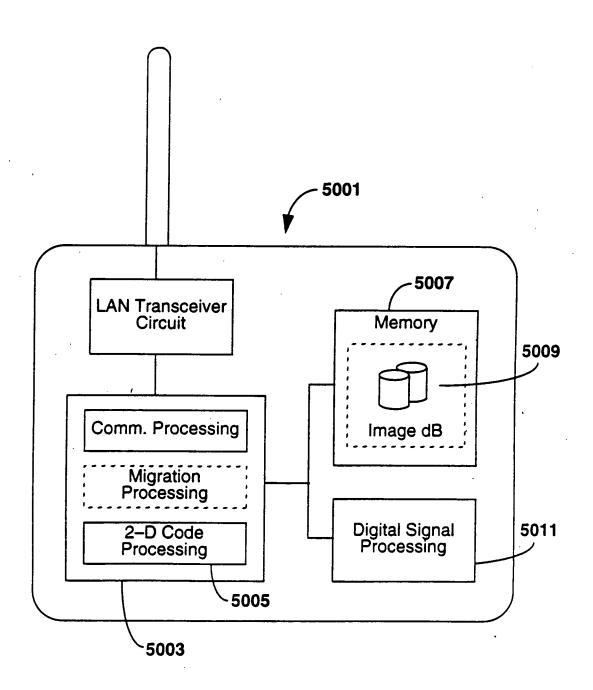


Fig. 50

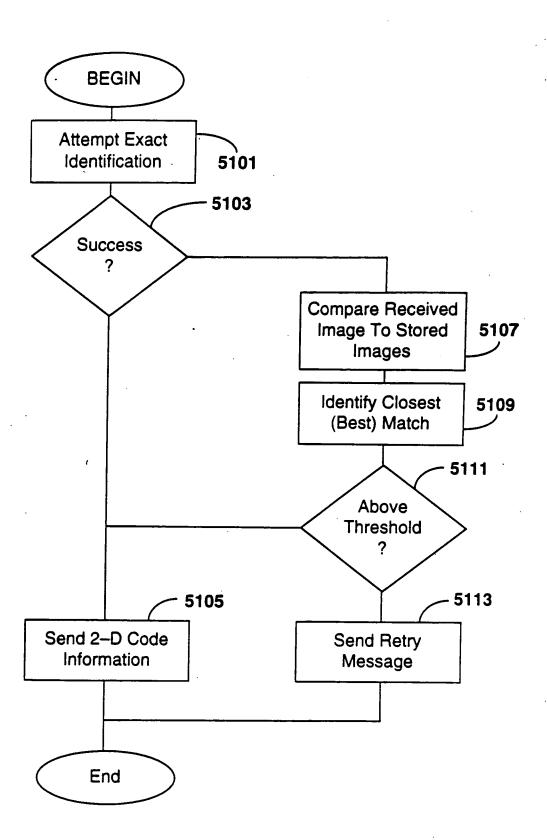


Fig. 51a

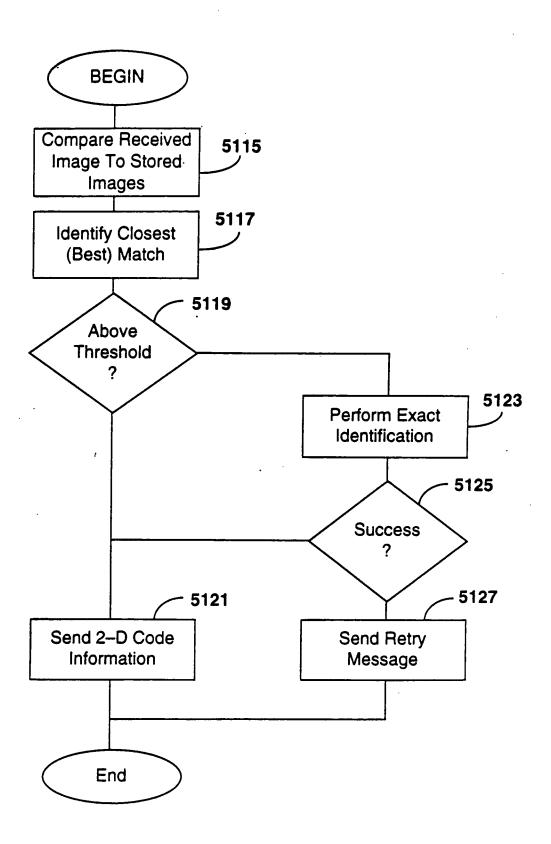


Fig. 51b

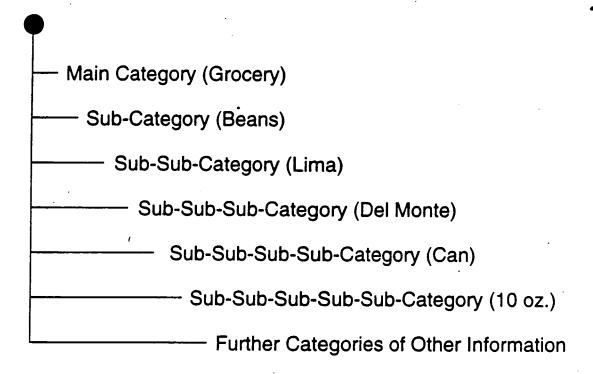


Fig. 52

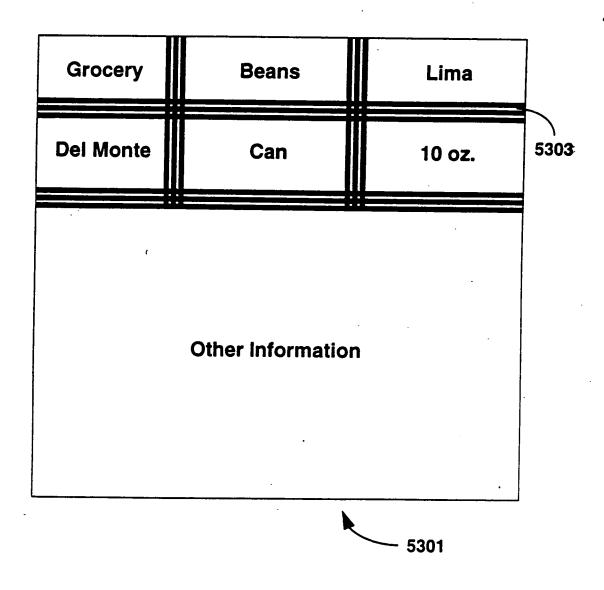


Fig. 53

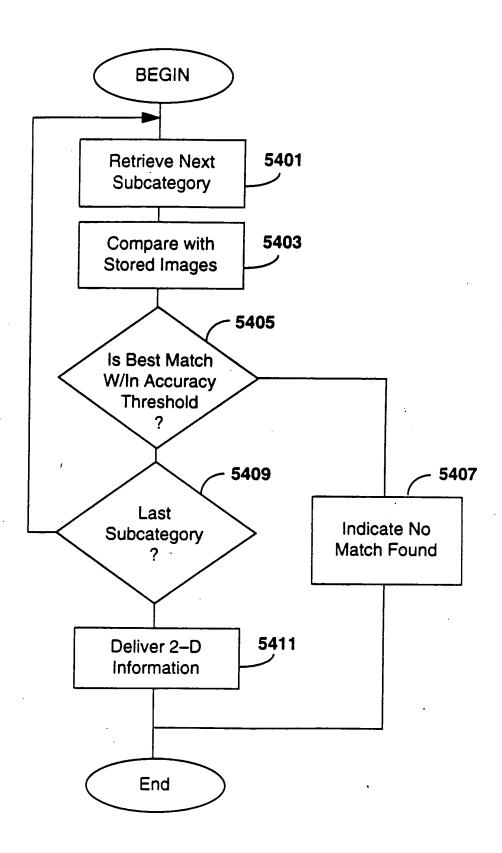


Fig. 54

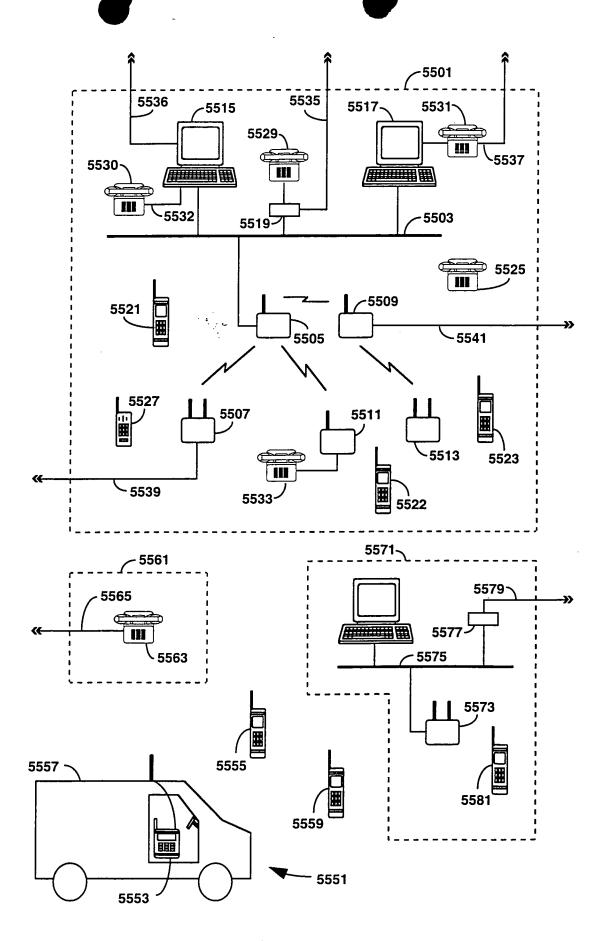


Fig. 55a

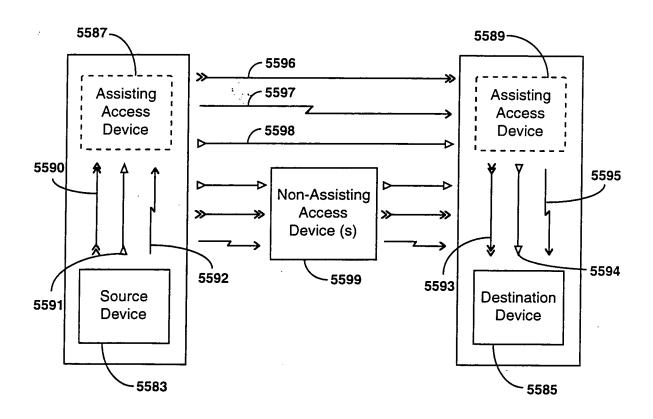


Fig. 55b

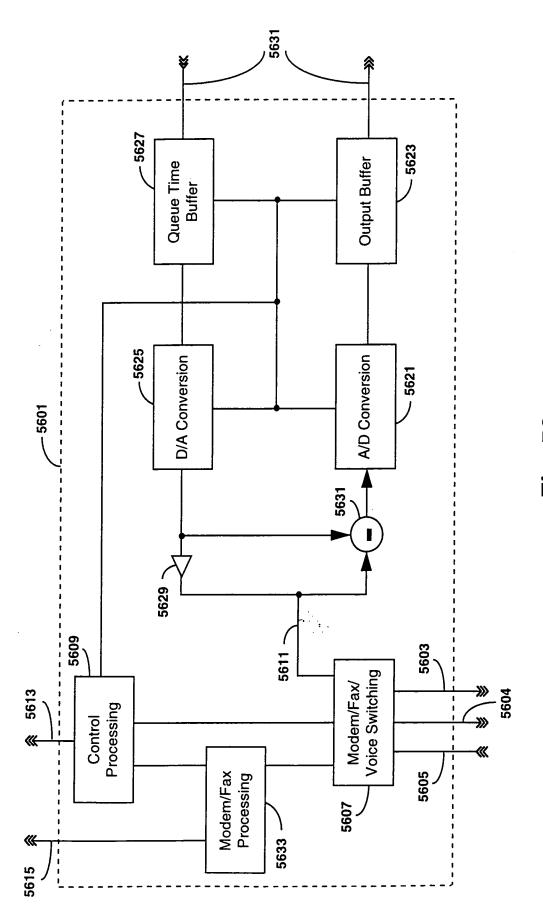


Fig. 56a

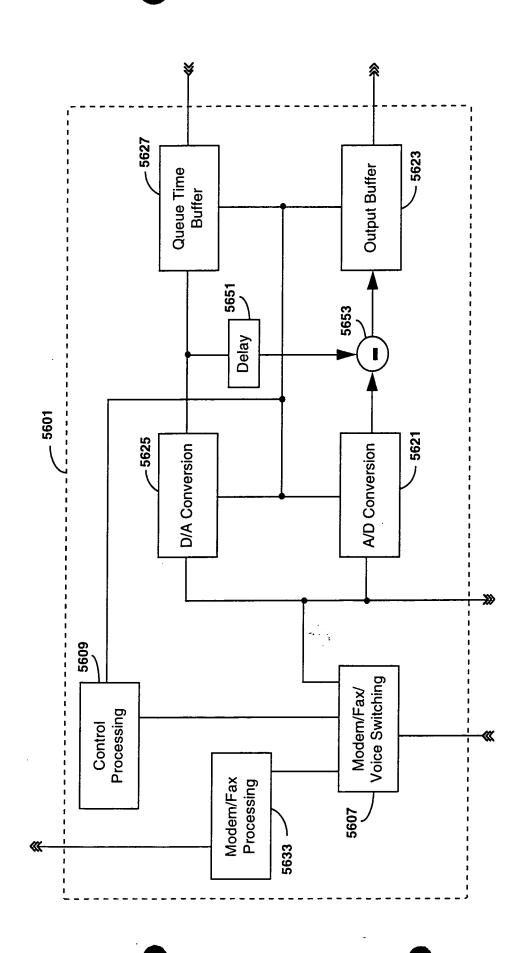


Fig. 56b

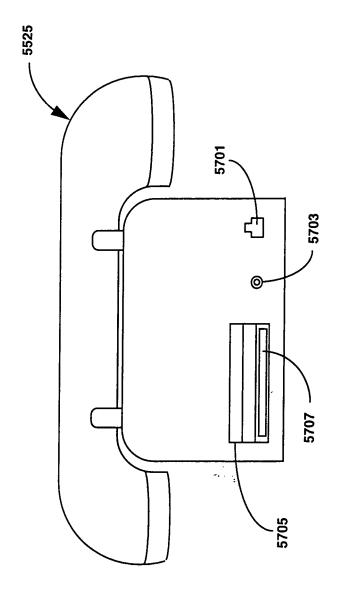


Fig. 57

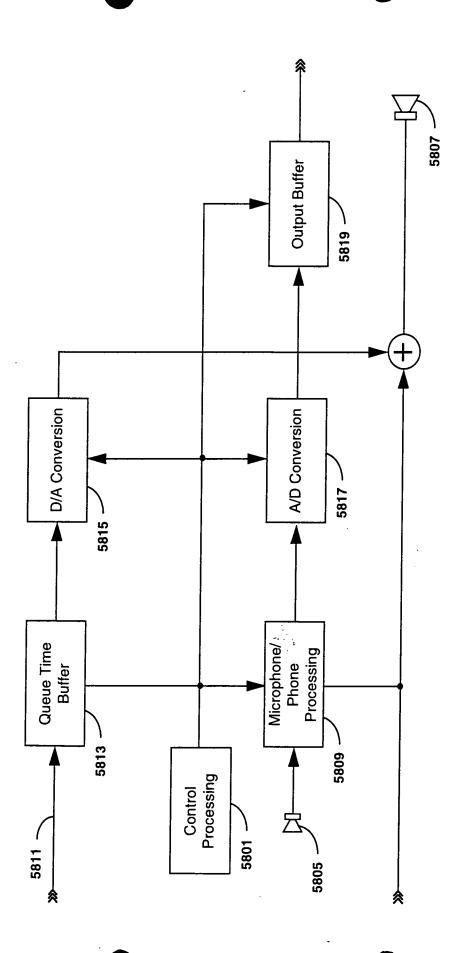


Fig. 58

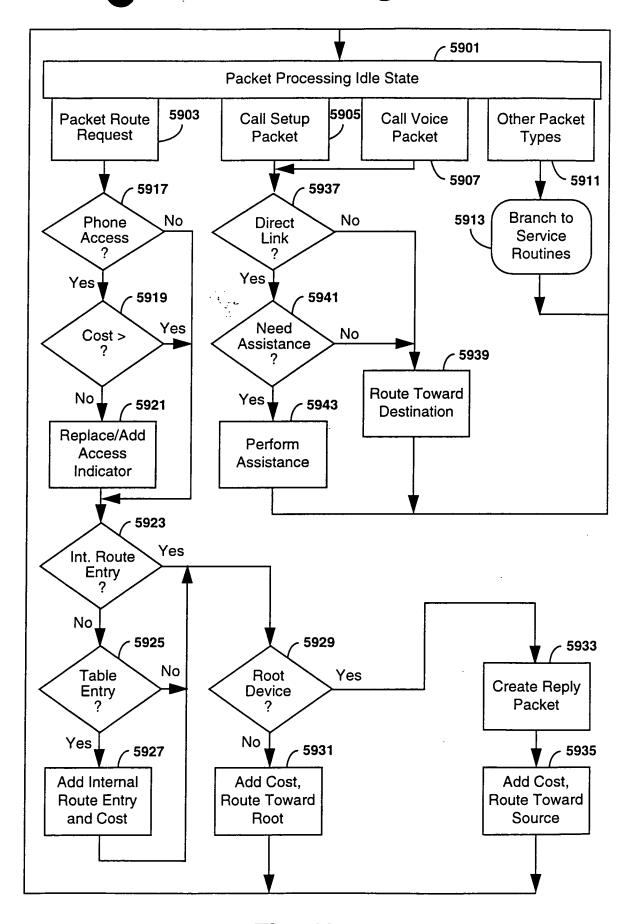


Fig. 59

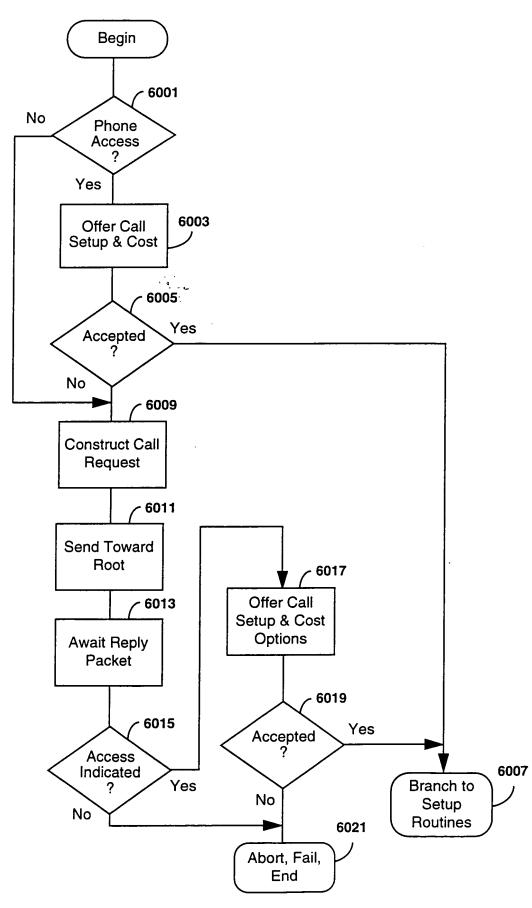


Fig. 60

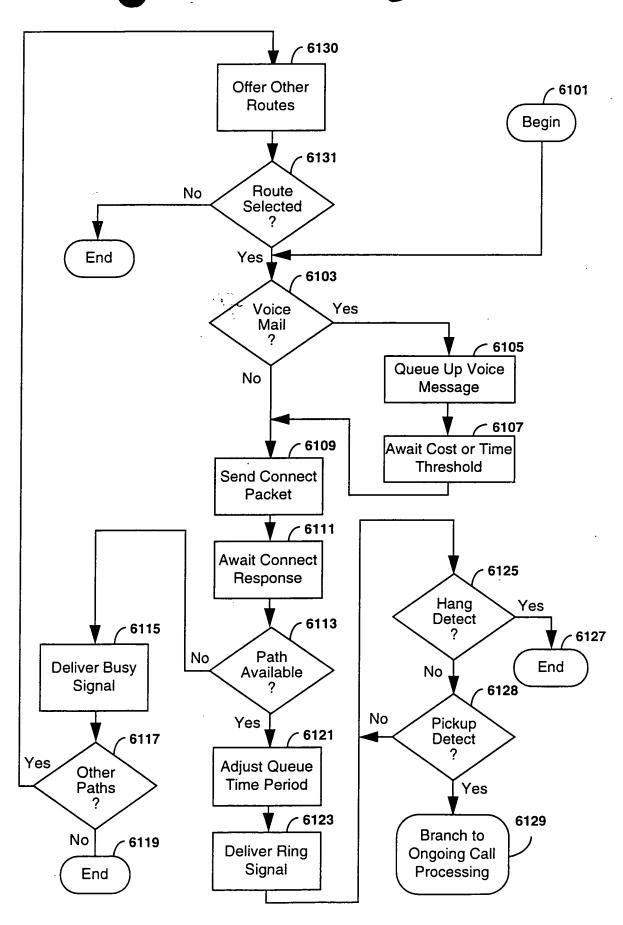


Fig. 61

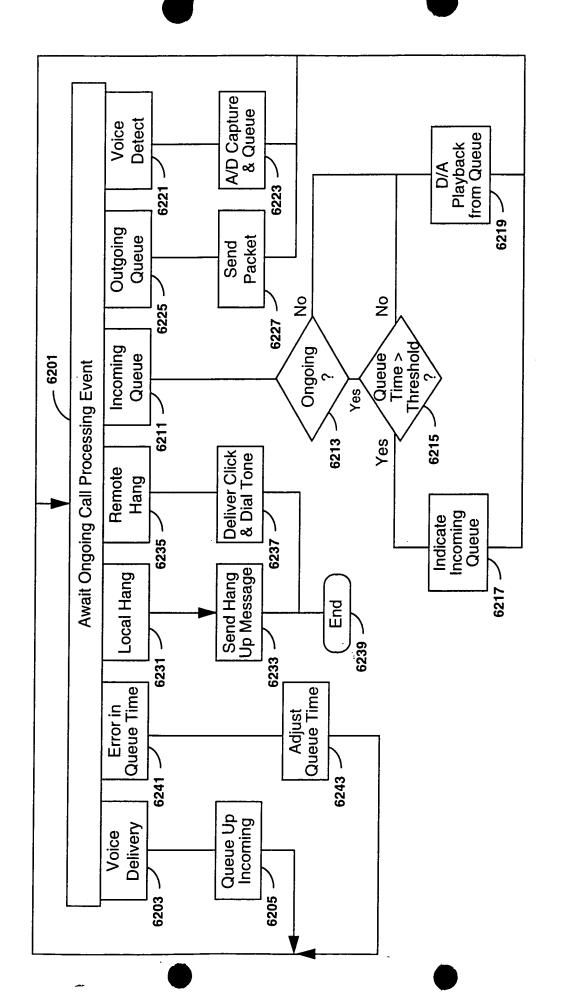


Fig. 62

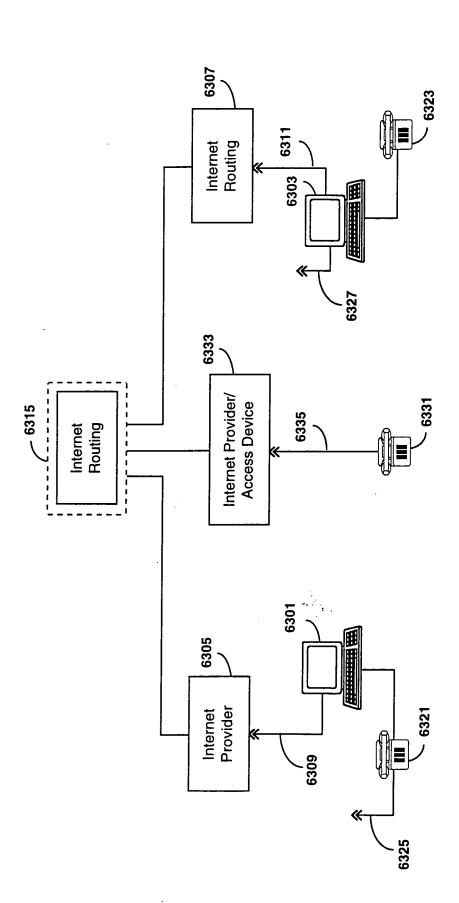


Fig. 63